



PAIENIS
112025-0488

2661 #2
7.1.2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED

JUL 24 2002

In Re The Application of:
John B. Duffie III et al.

Serial No.: 10/087,376

Filed: March 1, 2002

For: METHOD TO OPTIMIZE THE
LOAD BALANCING OF PARAL-
LEL CO-PROCESSORS

Examiner: Not yet assigned

Art Unit: 2661

RECEIVED

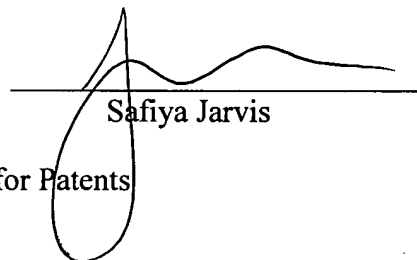
JUL 22 2002

Technology Center 2100

Cesari and McKenna, LLP
88 Black Falcon Avenue
Boston, MA 02210
July 15, 2002

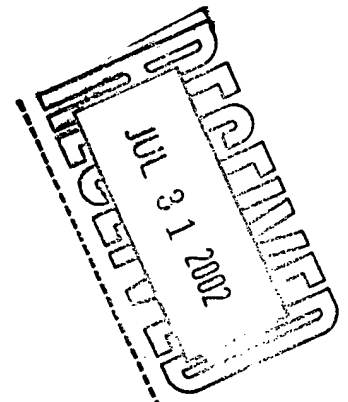
CERTIFICATE OF MAILING

I hereby certify that the following Information-Disclosure Statement is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on July 15, 2002.


Safiya Jarvis

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:



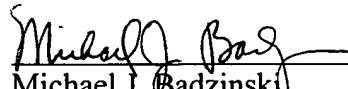
INFORMATION-DISCLOSURE STATEMENT

In keeping with the duty of candor and good faith owed to the Patent and Trade-
mark Office, Applicants wish to bring to the Examiner's attention the references listed on
the accompanying form PTO-1449. A copy of each listed reference is enclosed.

To the extent required by 37 C.F.R. §1.98(a)(3), Applicants have described what they consider to be the relevance of any foreign-language reference. The Office may find additionally relevant material in these or other references.

Please charge any additional fee occasioned by this paper to our Deposit Account No. 03-1237.

Respectfully submitted,


Michael J. Badzinski
Reg. No. P-51,425
CESARI AND MCKENNA, LLP
88 Black Falcon Avenue
Boston, MA 02210-2414
(617) 951-2500

+

Approved for use through 10/31/2002. OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Complete if Known

(use as many sheets as necessary)

Sheet	1	of	1
-------	---	----	---

Application Number	10/087,376
Filing Date	March 1, 2002
First Named Inventor	John B. DuffieIII et al.
Group Art Unit	2661
Examiner Name	Not yet assigned
Attorney Docket Number	112025-0488

[illegible][illegible]Date
Considered

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231.



PATENTS
112025-0488

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re The Application of:
John B. DuffieIII et al.

Serial No.: 10/087,376

Filed: March 1, 2002

For: METHOD TO OPTIMIZE THE
LOAD BALANCING OF PARAL-
LEL CO-PROCESSORS

Examiner: Not yet assigned

Art Unit: 2661

RECEIVED

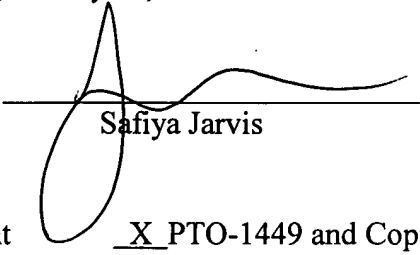
JUL 22 2002

Technology Center 2002

Cesari and McKenna, LLP
88 Black Falcon Avenue
Boston, MA 02210
July 15, 2002

CERTIFICATE OF MAILING

I hereby certify that the following paper is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on July 15, 2002.


Safiya Jarvis

X Information Disclosure Statement
X Return Receipt Postcard

X PTO-1449 and Copies of References